

L Number	Hits	Search Text	DB	Time stamp
1	1	5457687.pn. and logic	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/14 09:09
2	0	5457687.pn. and switch and arithmetical	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/14 09:09
3	0	5457687.pn. and switch and arithmetic	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/14 09:10
4	1	5457687.pn. and switch and Logic	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/14 09:10
5	1	5457687.pn. and switch and gate	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/14 09:10
6	1	5457687.pn. and gate	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/14 09:13
7	0	5457687.pn. and (switch same filter same gate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/14 09:13
8	1	5457687.pn. and (switch same filter)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/14 09:32
9	1	5457687.pn. and (filter same flip-flop)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/14 09:32

	157	("5852718" "5949272" "6061364" "6061364" "5854906" "5907485" "5587921" "5212652" "5233539" "5260881" "5329460" "5359536" "5422823" "5469368" "5490074" "5586044" "5598346" "5740069" "6128770" "6260174" "6393026" "6426952" "6091725" "5187671" "5495419" "5513369" "5530813" "5557533" "5623494" "5623684" "5636125" "5682322" "5742510" "5745363" "5781439" "5793644" "5815403" "5870313" "5875117" "5903461" "5914887" "5958030" "5963975" "5973506" "6016063" "6058144" "6097698" "6198324" "6205152" "6252448") .pn. ("6297668" "6333656" "6476736" "6538525" "3582902" "4860281" "4897786" "5018137" "5233604" "5255203" "5365520" "5390173" "5392033" "5408472" "5446726" "5448701" "5459722" "5519835" "5560038" "5600798" "5604867" "5625825" "5640399"	USPAT; US-PPGPUB	2003/10/08 13:16
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	66	@ad<20000831 and congestion\$3 and (hardware bus circuit) and (interconnect\$6 or wire) and (high adj speed).ab. and signal same buffer\$5	USPAT; US-PGPUB	2003/10/08 13:18
	32	@ad<20000831 and (congestion\$3 same control) and (hardware bus circuit) and (interconnect\$6 or wire) and (high adj speed).ab. and signal same buffer\$5	USPAT; US-PGPUB	2003/10/08 13:18
	6	@ad<20000831 and (congestion\$3 same control same signal) and (hardware bus circuit) and (interconnect\$6 or wire) and (high adj speed).ab. and signal same buffer\$5	USPAT; US-PGPUB	2003/10/08 13:28
	4	@ad<20000831 and (congestion\$3 same control same signal) and ((interconnect\$6 or wire) same circuit) and (high adj speed).ab. and signal same buffer\$5	USPAT; US-PGPUB	2003/10/08 14:20
	6166	@ad<20000831 and (segment\$8 divided division slice) adj4 (circuit line path interconnect) same signal and transmission and line	USPAT; US-PGPUB	2003/10/08 14:25
	96	@ad<20000831 and (segment\$8 divided division slice) adj4 circuit adj4 (line path interconnect) same signal and transmission and line	USPAT; US-PGPUB	2003/10/08 14:27
	0	@ad<20000831 and (segment\$8 divided division slice) adj4 circuit adj4 (line path interconnect) same signal and transmission and line and (congestion adj4 control)	USPAT; US-PGPUB	2003/10/08 14:28
	0	@ad<20000831 and (segment\$8 divided division slice) adj4 circuit adj4 (line path interconnect) same signal and transmission and line and (congestion)	USPAT; US-PGPUB	2003/10/08 14:29
	145761	@ad<20000831 and (segment\$8 divided division slice) adj4 circuit and (congestion) adj4 (line path interconnect) (data) adj4 (line path interconnect)	USPAT; US-PGPUB	2003/10/08 14:31
	5	@ad<20000831 and (segment\$8 divided division slice) adj4 circuit and (congestion) adj4 (line path interconnect) and (data) adj4 (line path interconnect)	USPAT; US-PGPUB	2003/10/08 14:54
	6329	@ad<20000831 and interconnect\$6.ti.	USPAT; US-PGPUB	2003/10/08 14:37
	91500	@ad<20000831 and circuit.ti. interconnect\$6.ti.	USPAT; US-PGPUB	2003/10/08 14:38
	1040	@ad<20000831 and circuit.ti. and interconnect\$6.ti.	USPAT; US-PGPUB	2003/10/08 14:38
	0	@ad<20000831 and circuit.ti. and interconnect\$6.ti. and congestion\$3.ab.	USPAT; US-PGPUB	2003/10/08 14:38
	1	@ad<20000831 and circuit.ti. and interconnect\$6.ti. and (bottleneck clogging congestion).ab.	USPAT; US-PGPUB	2003/10/08 14:40
	68	@ad<20000831 and (segment\$8 divided division slice) adj4 (line path interconnect trace) and (congestion) adj4 (line path interconnect) and (data)	USPAT; US-PGPUB	2003/10/08 14:53
	12	@ad<20000831 and (segment\$8 divided division slice) adj4 circuit and (congestion) adj4 (line path interconnect)	USPAT; US-PGPUB	2003/10/08 14:56
	57	@ad<20000831 and circuit adj board and (interconnect\$5.ti. interconnect\$5.ab.) and (line wire segment path trace) same congestion\$3	USPAT; US-PGPUB	2003/10/08 16:20
	1	@ad<20000831 and circuit adj board and (interconnect\$5.ti. interconnect\$5.ab.) and (line wire segment path trace) same congestion\$3	EPO; JPO; DERWENT; IBM_TDB	2003/10/08 16:21

	1	@ad<20000831 and circuit adj board and (interconnect\$5.ti. interconnect\$5.ab.) and (line wire segment path trace) same (congestion\$3 bottleneck\$4)	EPO; JPO; DERWENT; IBM_TDB	2003/10/08 16:21
	109	@ad<20000831 and circuit adj board and (interconnect\$5.ti. interconnect\$5.ab.) and (line wire segment path trace) same (congestion\$3 bottleneck\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/08 16:27
	104	@ad<20000831 and circuit adj board and (interconnect\$5.ti. interconnect\$5.ab.) and (line wire segment path trace) same (congestion\$3 bottleneck\$4)NOT FPGA	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/08 16:29
	84	@ad<20000831 and circuit adj board and (interconnect\$5.ti. interconnect\$5.ab.) and (line wire segment path trace) same (congestion\$3 bottleneck\$4)NOT FPGA and (buffer\$5 stor\$5 memor\$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/08 16:32
	46	@ad<20000831 and PCB and (interconnect\$5.ti. interconnect\$5.ab.) and (line wire segment path trace) same (congestion\$3 bottleneck\$4)NOT FPGA and (buffer\$5 stor\$5 memor\$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/08 17:03
	1	@ad<20000831 and PCB and (interconnect\$5.ti. interconnect\$5.ab.) and (line wire segment path trace) same (congestion\$3 bottleneck\$4)NOT FPGA and (buffer\$5 stor\$5) and 6263422.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/08 17:04
	1	@ad<20000831 and PCB and (interconnect\$5.ti. interconnect\$5.ab.) and (congestion\$3 bottleneck\$4)NOT FPGA and (buffer\$5 stor\$5) and 6263422.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/08 17:05
	1	@ad<20000831 and PCB and (interconnect\$5.ti. interconnect\$5.ab.) and (congestion\$3 bottleneck\$4)NOT FPGA and 6263422.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/08 17:06
	0	@ad<20000831 and 710/\$.ccls	USPAT; US-PGPUB	2003/10/09 14:20
	13970	@ad<20000831 and 710/\$.ccls.	USPAT; US-PGPUB	2003/10/09 14:20
	250	@ad<20000831 and 710/\$.ccls. and congestion	USPAT; US-PGPUB	2003/10/09 14:20
	194	@ad<20000831 and 710/\$.ccls. and congestion and buffer\$5	USPAT; US-PGPUB	2003/10/09 14:22
	61	@ad<20000831 and 710/\$.ccls. and congest\$6 same signal and buffer\$5	USPAT; US-PGPUB	2003/10/09 14:38
	1003	@ad<20000831 and 710/\$.ccls. and flow same control and buffer\$5 and data adj (line wire interconnect)	USPAT; US-PGPUB	2003/10/09 14:41
	284	@ad<20000831 and 710/\$.ccls. and flow same control and buffer\$5 and data adj (line wire interconnect) and segments	USPAT; US-PGPUB	2003/10/09 15:43
	45629	@ad<20000831 and transmission adj2 line	USPAT; US-PGPUB	2003/10/09 14:47
	41833	@ad<20000831 and transmission adj line	USPAT; US-PGPUB	2003/10/09 14:48
	33	@ad<20000831 and transmission adj line and (congestion backoff bottleneck) adj2 signal	USPAT; US-PGPUB	2003/10/09 15:43
	3101	@ad<20000831 and 370/\$.ccls. and congestion	USPAT; US-PGPUB	2003/10/09 15:44
	2091	@ad<20000831 and 370/\$.ccls. and congestion and buffer\$5	USPAT; US-PGPUB	2003/10/09 15:45
	334	@ad<20000831 and 370/\$.ccls. and congestion and buffer\$5 adj2 switch\$2	USPAT; US-PGPUB	2003/10/09 15:48
	125	@ad<20000831 and 370/\$.ccls. and congestion adj control and buffer\$5 adj2 switch\$2	USPAT; US-PGPUB	2003/10/09 16:01

-	0	5638291.pn. and (congest\$3 congestion)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 16:01
-	0	5638291.pn. and (bottleneck)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 16:01
-	8	@ad<20000831 and 370/\$.ccls. and (congest\$6 adj signal) and buffer\$5 adj2 switch\$2	USPAT; US-PGPUB	2003/10/09 16:28
-	12	@ad<20000831 and 370/\$.ccls. and (congest\$6 adj signal) and buffer\$5 adj2 data	USPAT; US-PGPUB	2003/10/09 16:08
-	0	6614756.pn. and 6512743.pn. and congest\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 16:16
-	2	6614756.pn. 6512743.pn. and congest\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/09 16:23
-	0	@ad<20000831 and 370/\$.ccls. and beckn	USPAT; US-PGPUB	2003/10/09 16:28
-	1	@ad<20000831 and 370/\$.ccls. and backn	USPAT; US-PGPUB	2003/10/09 16:29
-	85	@ad<20000831 and becn and switch\$3 and buffer\$6	USPAT; US-PGPUB	2003/10/09 16:40
-	51	@ad<20000831 and becn and switch\$3 same buffer\$6	USPAT; US-PGPUB	2003/10/09 16:40
-	13	@ad<20000831 and becn and switch\$3 adj3 buffer\$6	USPAT; US-PGPUB	2003/10/09 16:54
-	51	@ad<20000831 and becn and switch\$3 same buffer\$6	USPAT; US-PGPUB	2003/10/09 16:54
-	1	5457687.pn. and switch and becn	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 10:14
-	1	5457687.pn. and switch same becn	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 10:06
-	1	5457687.pn. and switch same buffer\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 10:11
-	2	5457687.pn. and destination and becn	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 11:39
-	1	5457687.pn. and destination same becn	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 10:11
-	0	5457687.pn. and multiple same paths	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 10:15
-	1	5457687.pn. and paths	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 10:23

	1	5457687.pn. and parallel	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 10:28
	1	5457687.pn. and store	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 10:28
	71	@ad<20000831 and transmission adj lines and driving adj circuits and store and logical	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 10:31
	0	@ad<20000831 and transmission adj lines and driving adj circuits and store same logical adj functions	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 10:32
	6	@ad<20000831 and transmission adj lines and driving adj circuits and store same logical	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 10:37
	0	@ad<20000831 and wire and driving adj circuits and store same digital same logic	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 10:37
	64	@ad<20000831 and wire and driving adj circuits and store same data same logic	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 10:38
	12	@ad<20000831 and driving adj circuits same data adj3 switch\$3 same store	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 10:42
	0	@ad<20000831 and driving adj circuits same atm adj3 switch\$3 same store	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 10:43
	1	@ad<20000831 and driving adj circuits same atm adj3 switch\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 10:44
	1	5495186.pn. and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 10:45
	0	5495186.pn. and atm adj switch and store	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 10:49
	0	5495186.pn. and store and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 10:50
	1	5495186.pn. and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 10:53

-	0	@ad<2000831 and atm adj switch and buffer and digital adj logic	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 10:54
-	0	@ad<2000831 and atm adj switch and buffer and logical adj function	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 10:57
-	0	@ad<2000831 and atm adj switch and buffer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 10:57
-	11	@ad<20000831 and atm adj switch and buffer and digital adj logic	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 10:58
-	4	@ad<20000831 and atm adj switch and buffer same digital adj logic	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 11:20
-	1143	@ad<20000831 and atm adj switch same buffer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 11:21
-	70	@ad<20000831 and network adj3 switch same buffer and logic adj (operation function gate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 11:23
-	16	@ad<20000831 and (network adj switch).ti. and buffer and logic adj (operation function gate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 11:24
-	1	5719862.pn. and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 11:34
-	1	5719862.pn. and switch same buffer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 11:34
-	2	5457687.pn. and congestion same signals	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 11:39
-	2	5457687.pn. and congestion same signals	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 11:55
-	2	5457687.pn. and congestion same signal and source	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 11:55
-	2	5457687.pn. and congestion same signal same source	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 13:37

	1	5457687.pn. and interconnect and circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 13:57
	2	5457687.pn. and node and congestion adj2 signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 14:20
	1	5457687.pn. and interconnect\$5 and circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 14:27
	2	5457687.pn. and destination and congestion adj2 signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 14:28
	2	5457687.pn. and destination same congestion adj2 signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 14:31
	1	5457687.pn. and destination same generate same congestion	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 14:47
	0	5457687.pn. and pluarity	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 14:48
	1	5457687.pn. and plurality	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/14 09:06
	0	5457687.pn. and selectively	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/10 16:50